

In the claims

1(Previously Presented). A system for read path acceleration, comprising:

a memory core divided into a plurality of segments each segment having a plurality of local amplifiers each coupled to a pair of global read data lines; and
a main amplifier having an input coupled to the pair of global read data lines and having an output coupled to an output register; and
a main amplifier strobe coupled to each of the plurality of local amplifiers.

2(Cancelled).

3(Currently Amended). The system of claim 21, further including a second main amplifier strobe coupled to the main amplifier.

4(Currently Amended). The system of claim 3, wherein the second main amplifier strobe is coupled to an equalization circuit.

5(Original). The system of claim 1, further including a local strobe reset circuit coupled to a local amplifier of the plurality of local amplifiers.

6(Original). The system of claim 5, wherein the local strobe reset circuit has an input coupled to the main amplifier strobe.

7(Original). The system of claim 6, wherein the local strobe reset circuit has an input coupled to a local amplifier strobe.

8(Previously Presented). A system for read path acceleration, comprising:

a plurality of strobe reset circuits; and

a plurality of local amplifier strobe circuits each having an input coupled to an output of one of the plurality of strobe reset circuits, wherein each of the plurality of strobe reset circuits has an input coupled to a strobe off signal from each of the plurality of local amplifier strobe circuits.

9(Cancelled).

10(Original). The system of claim 8, further including a main amplifier strobe circuit coupled to an input of each of the plurality of strobe reset circuits.

11(Original). The system of claim 10, wherein each of the plurality of strobe reset circuits delays a rising edge of a global strobe end signal from the main amplifier strobe circuit.

12(Original). The system of claim 8, further including a main amplifier coupled to a pair of global read data lines, the pair of global read data lines coupled to a local amplifier coupled to one of the plurality of local amplifier strobe circuits.

13(Original). The system of claim 12, further including a second main amplifier strobe circuit coupled to the main amplifier.

14(Original). The system of claim 10, wherein the main amplifier strobe delays a rising edge of a global word strobe signal.

15(Original). The system of claim 14, wherein the main amplifier strobe does not delay a falling edge of the global word strobe signal.

16(Original). A system for read path acceleration, comprising:

a first strobe reset circuit coupled to a first local amplifier;

a second strobe reset circuit coupled to a second local amplifier; and

a main amplifier coupled to an output of the first local amplifier and an output of the second local amplifier.

17(Original). The system of claim 16, wherein a first delay between the main amplifier and the first local amplifier is different from a second delay between the main amplifier and the second local amplifier.

18(Original). The system of claim 16, further including a main amplifier strobe circuit coupled to the first strobe reset circuit and the second strobe reset circuit.

19(Original). The system of claim 18, further including a second main amplifier strobe circuit coupled to the main amplifier.

20(Original). The system of claim 19, further including an equalization circuit coupled to the main amplifier strobe circuit.